



DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that:
my residence, post office address and citizenship are as stated next to my name.

I declare the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **QUAD FLAT NO LEAD (QFN) GRID ARRAY PACKAGE, METHOD OF MAKING MEMORY MODULE AND COMPUTER SYSTEM INCLUDING SAME**, the specification of which (check one):

- ☐ is attached hereto.
☒ was filed on August 20, 2001 as United States application serial no. 09/933,297.
☐ was filed on _____ as PCT international application no. _____ and was amended under PCT Article 19 on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

Prior foreign/PCT application(s):

<u>200104675-4</u> (number)	<u>Singapore</u> (country)	<u>06/08/2001</u> (day/month/year filed)	Priority Claimed	
			<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No
(number)	(country)	(day/month/year filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available between the filing date of such prior application and the national or PCT international filing date of this application:

_____ (application serial no.)	_____ (filing date)	_____ (status - pending, patented or abandoned)
_____ (application serial no.)	_____ (filing date)	_____ (status - pending, patented or abandoned)

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

_____ (provisional application no.)	_____ (filing date)
--	------------------------

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

David V. Trask, Reg. No. 22,012
Joseph A. Walkowski, Reg. No. 28,765
Allen C. Turner, Reg. No. 33,041
Brick G. Power, Reg. No. 38,581
Krista Weber Powell, Reg. No. 47,867
Shawn G. Hansen, Reg. No. 42,627
Katherine A. Hamer, Reg. No. 47,628

William S. Britt, Reg. No. 20,969
James R. Duzan, Reg. No. 28,393
Edgar R. Cataxinos, Reg. No. 39,931
Paul C. Oestreich, Reg. No. 44,983
Jarett K. Abramson, Reg. No. 47,376
Bretton L. Crockett, Reg. No. 44,632
Michael L. Lynch, Reg. No. 30,871

Laurence B. Bond, Reg. No. 30,549
H. Dickson Burton, Reg. No. P-48,396
Kent S. Burningham, Reg. No. 30,453
Devin R. Jensen, Reg. No. 44,805
David L. Stott, Reg. No. 43,937
Bradley B. Jensen, Reg. No. 46,801
Charles B. Brantley II, Reg. No. 38,086

Address all correspondence to: Bradley B. Jensen, telephone no. (801) 532-1922.
TRASKBRITT, PC
P.O. BOX 2550
Salt Lake City, Utah 84110

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Setho Sing Fee

Inventor's signature _____

Residence: Singapore

Citizenship: Republic of Singapore

Post Office Address: Block 91, Bedok North Street 4, #11-1523, Singapore 460091

Date _____

10/29/01

DECLARATION FOR PATENT APPLICATION
(continuation page)

Invention title: QUAD FLAT NO LEAD (QFN) GRID ARRAY PACKAGE, METHOD OF MAKING MEMORY MODULE AND COMPUTER SYSTEM INCLUDING SAME

Inventor name(s) appearing on first declaration page: Setho Sing Fee

☒ Additional original, first and joint inventor(s):

Full name of second joint inventor: Lim Thiam Chye

Inventor's signature

Residence: Singapore

Citizenship: Republic of Singapore

Post Office Address: Block 224, Simei Street 4 #11-98, Singapore 520224

Date

10/29/01